

Title: Subsystem Quantum Error Correcting Codes

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Abstract: <span>The essential insight of quantum error correction was that quantum information can be protected by suitably encoding this quantum information across multiple independently erred quantum systems. Recently it was realized that, since the most general method for encoding quantum information is to encode it into a subsystem, there exists a novel form of quantum error correction beyond the traditional quantum error correcting subspace codes. These new quantum error correcting subsystem codes differ from subspace codes in that their quantum correcting routines can be considerably simpler than related subspace codes. Here we present a class of quantum error correcting subsystem codes constructed from two classical linear codes. These codes are the subsystem versions of the quantum error correcting subspace codes which are generalizations of Shor's original quantum error correcting subspace codes. For every Shor-type code, the codes we present give a considerable savings in the number of stabilizer measurements needed in their error recovery routines.</span>



*IV Canadian Quantum Information  
Student's Conference  
Perimeter Institute  
Waterloo, June 1-5 2007*



**Quantum Error Correction**  
**Fault tolerant Quantum Computation**  
**&**  
**-Subsystem Codes-**

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# Talk Roadmap

## ● *Quantum noise a*

# Talk Roadmap

- *Quantum noise and environment interaction*
- *Classical  $E_r$*

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- *Stabilizer Quantum Error Correction*
- *Subsystem Coding*
- *Bacon-Shor's Codes*

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# *Quantum Error Correction*

# Quantum Computing *Problems*

- Schroedinger equation describes closed systems evolution.
- Real systems cannot be considered closed because of the environment interaction.
- The *Quantum operations formalism*, is a toolset to describe quantum noise and open systems behaviour.
- Quantum computers need a full control of the quantum interactions to be reliable.



# Environment *interact*

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Open system analysis describes the environment and the system as a unique object.

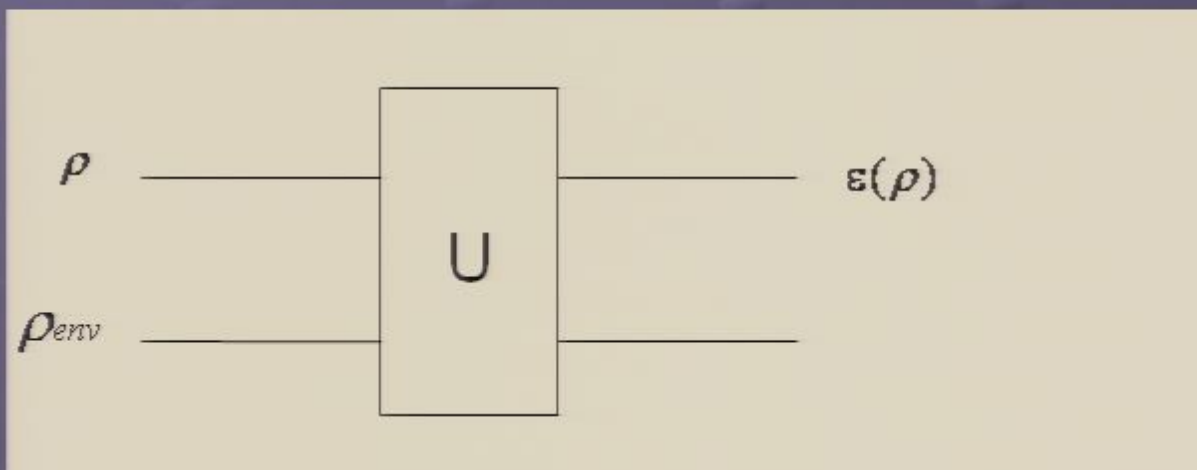
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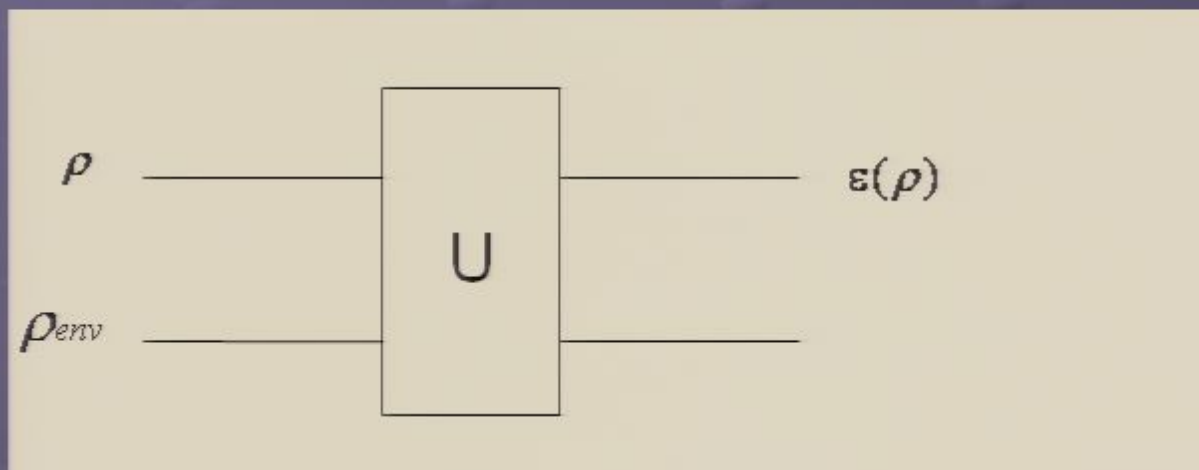


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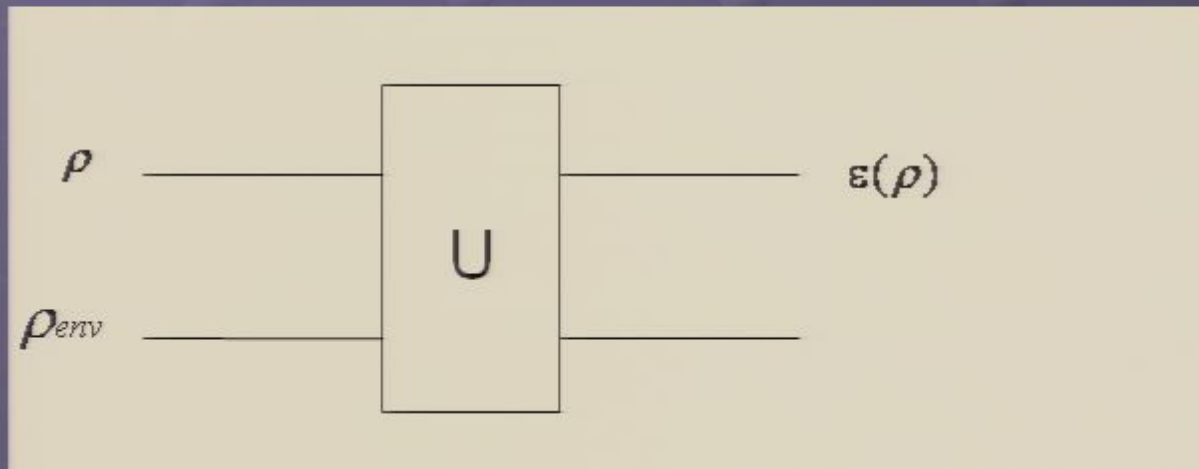
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The partial trace operator gives back the state of the system after the interaction with the environment

# Noise Linearization

• For  $\rho_{\text{in}} = \rho_{\text{out}} = \rho_{\text{in}}$   $\hat{\rho}_{\text{in}} = \hat{\rho}_{\text{out}} = \rho_{\text{in}}$

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$$0 < k < d^2$$

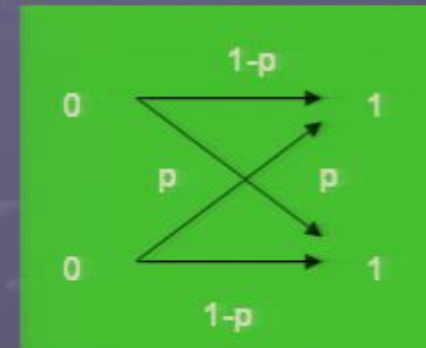


# Noise ope

# Noise operators on qubit

$$\varepsilon(\rho) = E_0 \rho E_0^\dagger + E_1 \rho E_1^\dagger$$

Channel  
Model



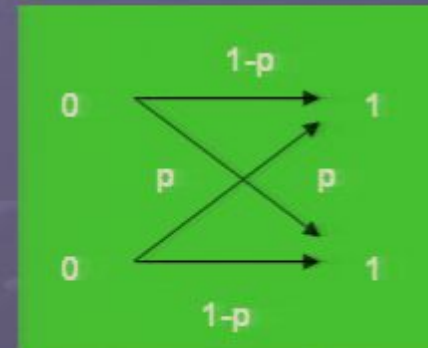
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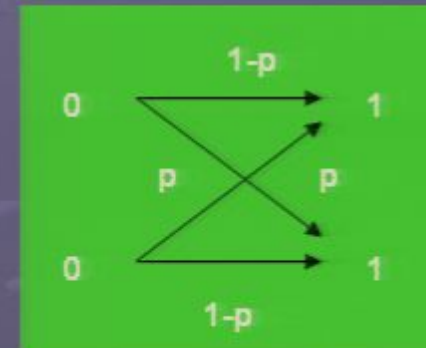


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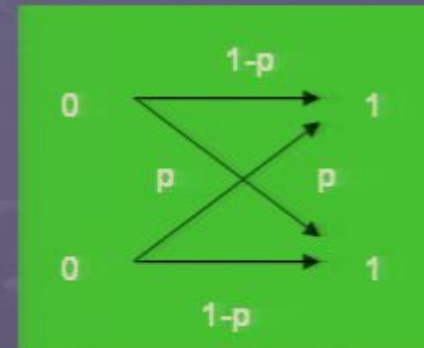
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# Classical Error correction

- The key idea of error correction is that we need to add redundancy bits to protect Information from noise. In this way it is possible (under certain conditions) to rebuild the information content.
- Linear error correcting codes are the simplest ones.
- A linear code is defined as a **vector subspace** on  $GF(n)$ .

$$\begin{pmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & 1 \end{pmatrix} \begin{pmatrix} 1 \\ 0 \\ 0 \\ 0 \end{pmatrix} = \begin{pmatrix} 1 \\ 0 \\ 0 \\ 0 \end{pmatrix} \quad \begin{pmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & 1 \end{pmatrix} \begin{pmatrix} 0 \\ 1 \\ 0 \\ 0 \end{pmatrix} = \begin{pmatrix} 0 \\ 1 \\ 0 \\ 0 \end{pmatrix}$$

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- E.G.: **ASCII code** is a linear code on  $GF(2)$ .

# *Linear Error correcting $\mathcal{C}$*



## Linear Error correcting codes

- An  $n$  bit information coding with a  $k$  bit CODE is defined by a generator matrix  $G$   $n \times k$  whose elements belong to  $GF(2)$ .
- An  $x$  message of  $n$  bits, is encoded in a message  $x_c$  of  $k$  bit by the following operation

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a  $k$  bit coding used to encode  $n$  bit of Information is denoted by the  $[n,k]$  notation

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- Moreover suppose that  $x$  and  $y$  are two  $n$  bit words: the **Hamming distance** between  $x$  and  $y$  is defined as the number of bits (in the same position) the two words differ from each other. For example:

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- In particular the **Code Hamming distance** is defined as :

$$d(C) \equiv \min_{x,y \in C, x \neq y} d(x, y)$$

*An example:*

*Th*

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*The Repetition Code*



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- The simplest way to protect one bit is to copy it three times...

0 → 000

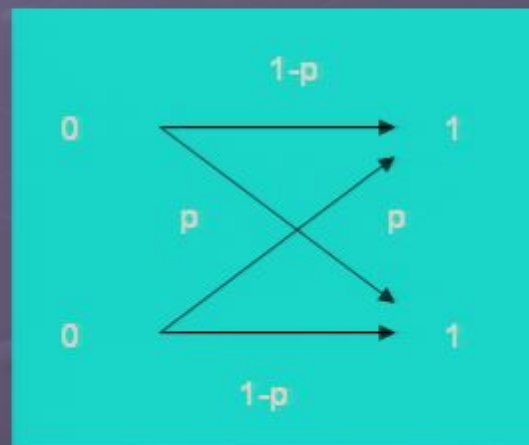
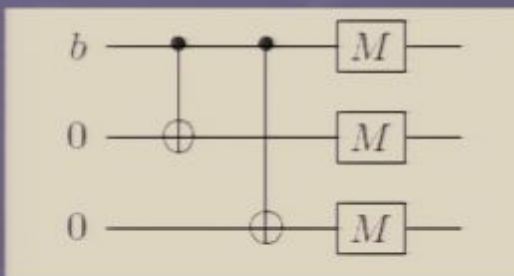
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$$M = \begin{bmatrix} 1-p & p \\ p & 1-p \end{bmatrix}$$

Suppose to have a 001 string for the channel output, the decoding circuit, "chooses" that the most likely event has been the **third bit flip** and it will decode the output as a

# Quantum codes

- We have to deal with three major problems when we want to build a quantum code :
  - **No cloning theorem** : it's impossible to duplicate an unknown quantum state.
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- Quantum error correcting rules are similar to classical ones and are denoted by the  $[[n,k,d]]$  notation where  $n$  is the number of qubit of the encoding,  $k$  is the number of encoded qubit while  $d$  is the code distance.

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- The **error syndromes** are obtained by 4 projective measurements

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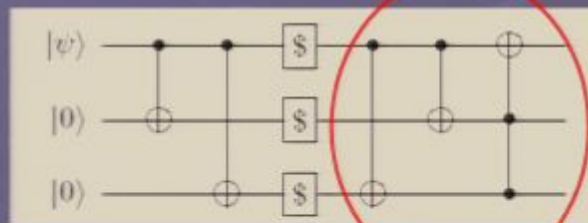
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# Pauli Group and Stabilizer Codes

- Pauli matrixes form an algebraic group called **Pauli group**,  $G_n$  with the tensor product of  $n$  Pauli operators each of them acting on one of the  $n$  qubit. For example

$$Z_1 Z_2 = Z \otimes Z \otimes I$$

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- The **Stabilizer subgroup** is defined as the set of Pauli operators that “**stabilize**” the code subspace (+1 eigenvalue). For example for the repetition code subspace we have

$$|000\rangle, |111\rangle$$



$$S = \langle Z_1 Z_2, Z_2 Z_3 \rangle$$

# Error detection *tools*

- Stabilizer

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- In fact the +1,-1 eigenvalues of the generators measurements on the state and the commutation rules of Pauli group operators are the ingredients to detect any kind of errors

$$X_1|\psi\rangle = |\varphi\rangle$$

$$Z_1Z_2|\varphi\rangle = Z_1Z_2(X_1|\psi\rangle) =$$

$$= -X_1Z_1Z_2|\psi\rangle = -X_1|\psi\rangle = -|\varphi\rangle$$

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$$\begin{aligned} [\bar{X}_i, \bar{X}_i] &= 0 \\ [\bar{Z}_i, \bar{Z}_i] &= 0 \\ \{\bar{X}_i, \bar{Z}_i\} &= 0 \\ \{\bar{X}_i, \bar{Y}_i\} &= 0 \\ \{\bar{Z}_i, \bar{Y}_i\} &= 0. \end{aligned}$$

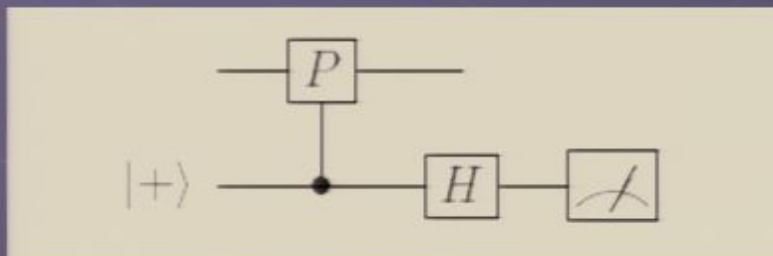
Commutation rules

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 [\bar{Z}_i, \bar{Z}_i] &= 0 \\
 \{\bar{X}_i, \bar{Z}_i\} &= 0 \\
 \{\bar{X}_i, \bar{Y}_i\} &= 0 \\
 \{\bar{Z}_i, \bar{Y}_i\} &= 0.
 \end{aligned}$$



Error detect

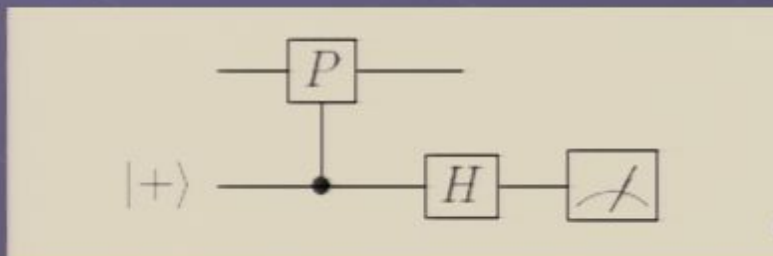
Commutation rules

# Error detection tools

- Stabilizer Codes help to simplify error detection procedures.
- In fact the +1,-1 eigenvalues of the generators measurements on the state and the commutation rules of Pauli group operators are the ingredients to detect any kind of errors

$$\begin{aligned}
 X_1 |\psi\rangle &= |\varphi\rangle \\
 Z_1 Z_2 |\varphi\rangle &= Z_1 Z_2 (X_1 |\psi\rangle) = \\
 &= -X_1 Z_1 Z_2 |\psi\rangle = -X_1 |\psi\rangle = -|\varphi\rangle \\
 -Z_1 Z_2 |\varphi\rangle &= |\varphi\rangle
 \end{aligned}$$

$$\begin{aligned}
 [\bar{X}_i, \bar{X}_i] &= 0 \\
 [\bar{Z}_i, \bar{Z}_i] &= 0 \\
 \{\bar{X}_i, \bar{Z}_i\} &= 0 \\
 \{\bar{X}_i, \bar{Y}_i\} &= 0 \\
 \{\bar{Z}_i, \bar{Y}_i\} &= 0.
 \end{aligned}$$



Error  
detection  
circuit

Commutation rules

# Example: Redundancy Code Error Syndromes

$Z_1 Z_2$	$Z_2 Z_3$	Error detected	Recovery action
+1	+1	No errors	no
+1	-1	3° Bit flip	<i>Operator: IIX</i>
-1	+1	1° Bit flip	<i>Operator: XII</i>
-1	-1	2° Bit flip	<i>Operator: IXI</i>



# Examples : Steane Code

Generator	Operators
$S_1$	$I \otimes X \otimes X \otimes I \otimes I \otimes X \otimes X$
$S_2$	$X \otimes I \otimes X \otimes I \otimes X \otimes I \otimes X$
$S_3$	$I \otimes I \otimes I \otimes X \otimes X \otimes X \otimes X$
$S_4$	$Z \otimes I \otimes Z \otimes I \otimes Z \otimes I \otimes Z$
$S_5$	$I \otimes I \otimes I \otimes Z \otimes Z \otimes Z \otimes Z$
$S_6$	$I \otimes Z \otimes Z \otimes I \otimes I \otimes Z \otimes Z$
$\bar{X}$	$X \otimes X \otimes X \otimes X \otimes X \otimes X \otimes X$
$\bar{Z}$	$Z \otimes Z \otimes Z \otimes Z \otimes Z \otimes Z \otimes Z$

Logical operations



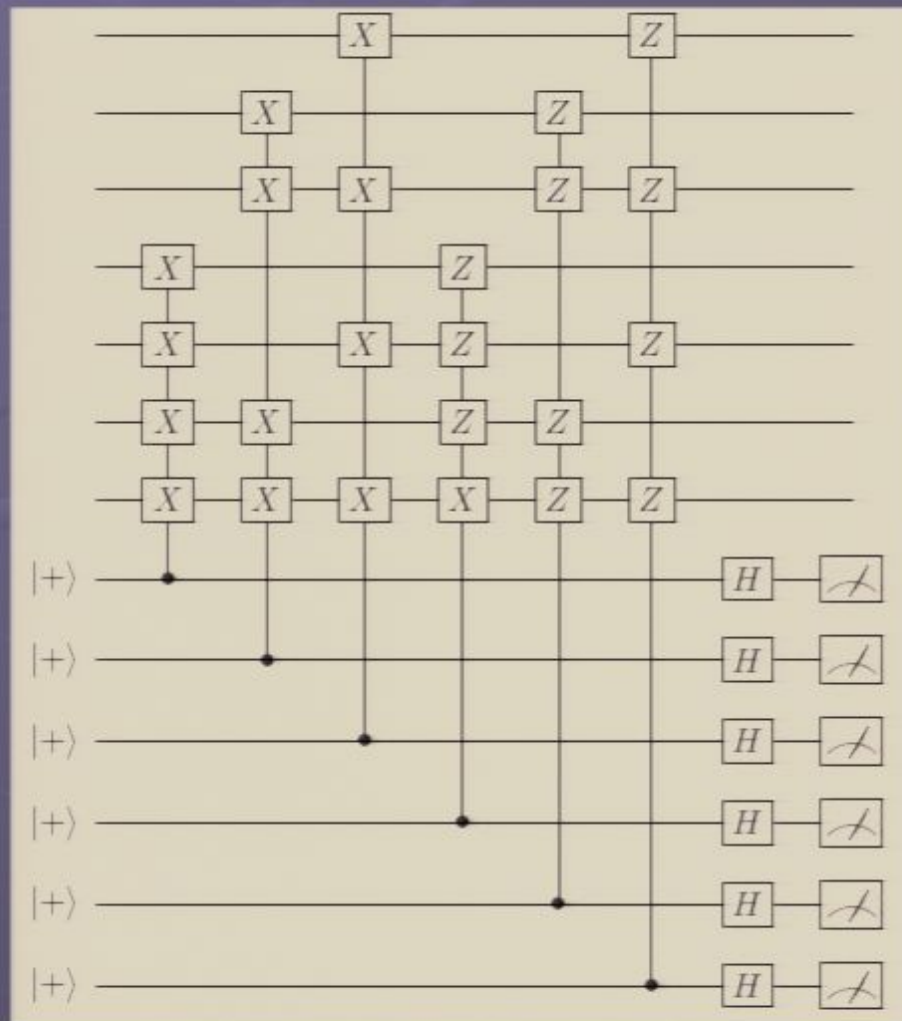
Operators that commute  
with the stabilizer subgroup:  
Error of this form can't be  
detected



# Steane code encoding-decoding circuit

Stabilizer encoding

Ancilla qubits



Syndrome detection

# Shor Code

$$|0_L\rangle = \frac{1}{2\sqrt{2}}(|000\rangle + |111\rangle) \otimes (|000\rangle + |111\rangle) \otimes (|000\rangle + |111\rangle)$$

$$|1_L\rangle = \frac{1}{2\sqrt{2}}(|000\rangle - |111\rangle) \otimes (|000\rangle - |111\rangle) \otimes (|000\rangle - |111\rangle)$$

Stabilizer generators:

ZZIIIIII

IIIZZZII

IIIIIZZZ

XXXXXXXX

IZZIIIIII

IIIZZZII

IIIIIZZZ

IIIXXXXX

Define subspace:

$$S|\psi\rangle = |\psi\rangle$$

Logical operators:

$$\bar{Z} = ZZZZZZZZ$$

$$\bar{X} = XXXXXXXX$$

$$\bar{Z}|0_L\rangle = |0\rangle$$

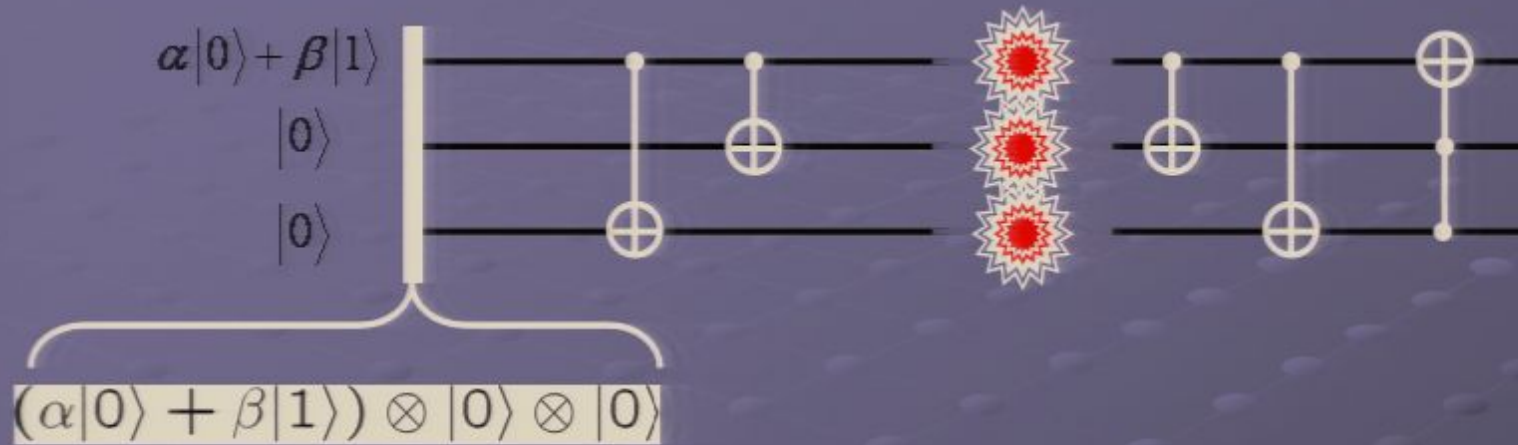
$$\bar{Z}|1_L\rangle = -|1\rangle$$

# Bit flip Cir

# Bit flip Circuit



# Bit flip Circuit



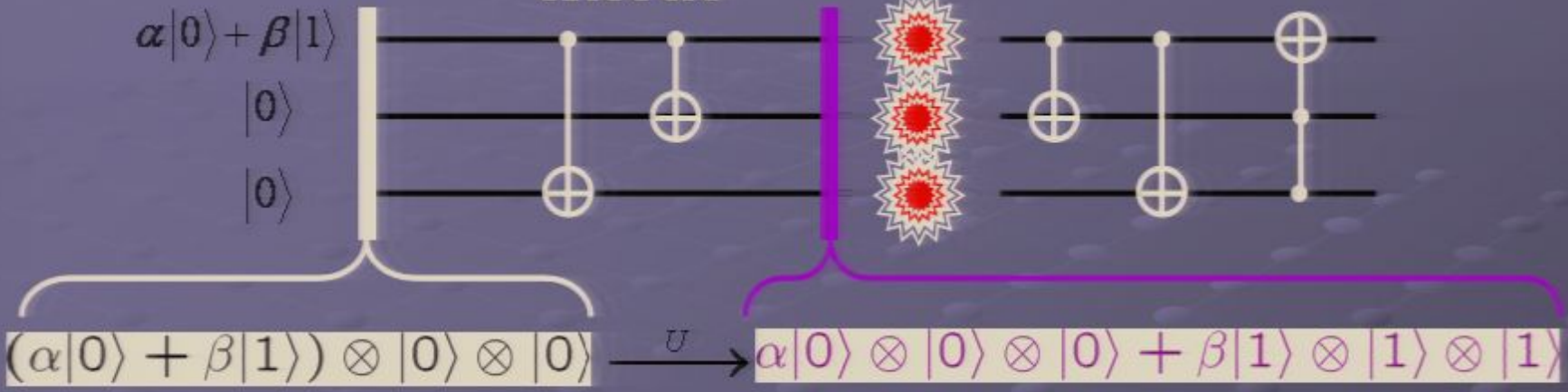
# Bit flip Circuit



1	0	0	0
0	1	0	0
0	0	0	1
0	0	1	0

encode

$\alpha|0\rangle + \beta|1\rangle$   
 $|0\rangle$   
 $|0\rangle$



$$(\alpha|0\rangle + \beta|1\rangle) \otimes |0\rangle \otimes |0\rangle \xrightarrow{U} \alpha|0\rangle \otimes |0\rangle \otimes |0\rangle + \beta|1\rangle \otimes |1\rangle \otimes |1\rangle$$

# Bit flip Circuit

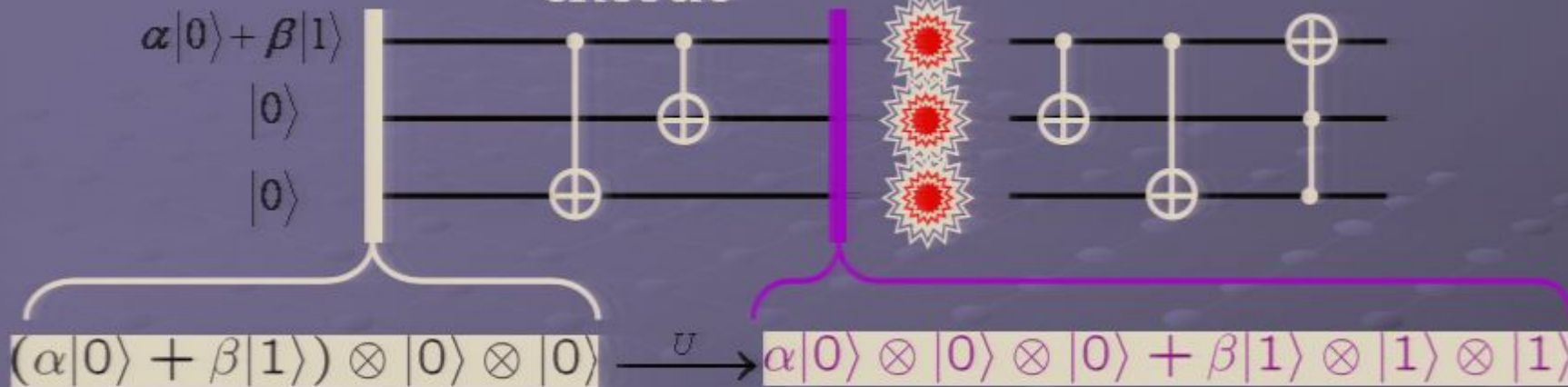
1	0	0	0
0	1	0	0
0	0	0	1
0	0	1	0

encode

$$\alpha|0\rangle + \beta|1\rangle$$

$$|0\rangle$$

$$|0\rangle$$



$$\alpha|000\rangle + \beta|111\rangle \xrightarrow{\text{XII}} \alpha|100\rangle + \beta|011\rangle \rightarrow (\alpha|1\rangle + \beta|0\rangle) |11\rangle \rightarrow (\alpha|0\rangle + \beta|1\rangle) |11\rangle$$

$$\alpha|000\rangle + \beta|111\rangle \xrightarrow{\text{IXI}} \alpha|010\rangle + \beta|101\rangle \rightarrow (\alpha|0\rangle + \beta|1\rangle) |10\rangle \rightarrow (\alpha|0\rangle + \beta|1\rangle) |10\rangle$$

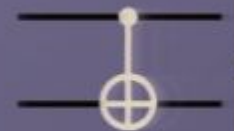
$$\alpha|000\rangle + \beta|111\rangle \xrightarrow{\text{IIX}} \alpha|001\rangle + \beta|110\rangle \rightarrow (\alpha|0\rangle + \beta|1\rangle) |01\rangle \rightarrow (\alpha|0\rangle + \beta|1\rangle) |01\rangle$$





$$\begin{aligned}
 \boxed{I} &= \begin{bmatrix} 1 & 0 \\ 0 & 1 \end{bmatrix} \\
 \boxed{X} &= \begin{bmatrix} 0 & 1 \\ 1 & 0 \end{bmatrix}
 \end{aligned}$$

# Bit flip Circuit

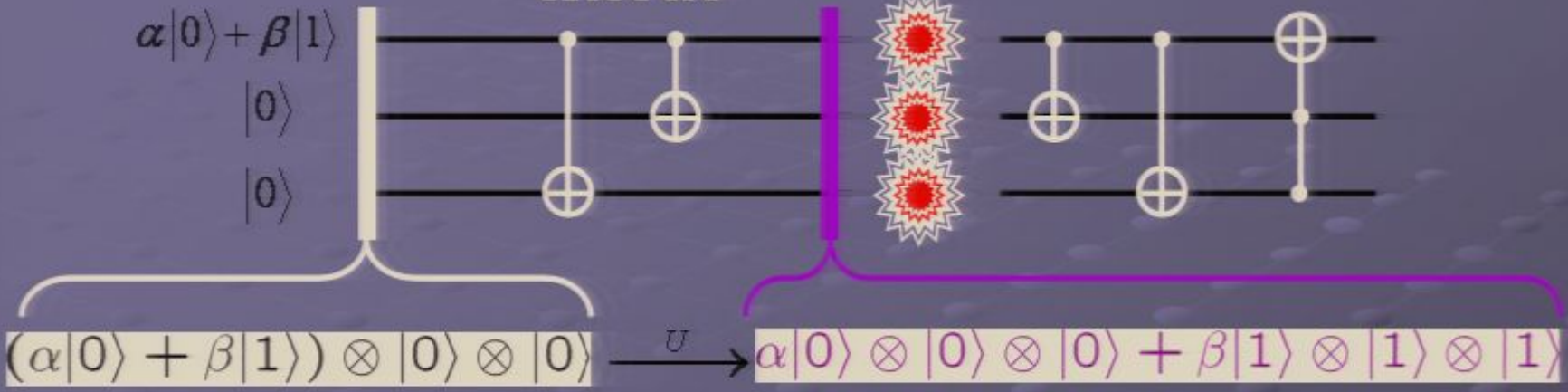


$$\begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 1 \\ 0 & 0 & 1 & 0 \end{bmatrix}$$

$$\begin{aligned}
 &\alpha|0\rangle + \beta|1\rangle \\
 &|0\rangle \\
 &|0\rangle
 \end{aligned}$$

encode

error



$$\begin{aligned}
 \alpha|000\rangle + \beta|111\rangle &\xrightarrow{\text{XII}} \alpha|100\rangle + \beta|011\rangle \rightarrow (\alpha|1\rangle + \beta|0\rangle) |11\rangle \rightarrow (\alpha|0\rangle + \beta|1\rangle) |11\rangle \\
 \alpha|000\rangle + \beta|111\rangle &\xrightarrow{\text{IXI}} \alpha|010\rangle + \beta|101\rangle \rightarrow (\alpha|0\rangle + \beta|1\rangle) |10\rangle \rightarrow (\alpha|0\rangle + \beta|1\rangle) |10\rangle \\
 \alpha|000\rangle + \beta|111\rangle &\xrightarrow{\text{IIX}} \alpha|001\rangle + \beta|110\rangle \rightarrow (\alpha|0\rangle + \beta|1\rangle) |01\rangle \rightarrow (\alpha|0\rangle + \beta|1\rangle) |01\rangle
 \end{aligned}$$



$$\begin{array}{|c|} \hline I \\ \hline \end{array} = \begin{array}{|cc|} \hline 1 & 0 \\ \hline 0 & 1 \\ \hline \end{array}$$

$$\begin{array}{|c|} \hline X \\ \hline \end{array} = \begin{array}{|cc|} \hline 0 & 1 \\ \hline 1 & 0 \\ \hline \end{array}$$

## Bit flip Circuit



1	0	0	0
0	1	0	0
0	0	0	1
0	0	1	0

$$\alpha|0\rangle + \beta|1\rangle$$

$$|0\rangle$$

$$|0\rangle$$

encode

error

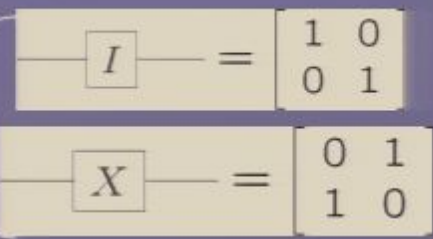
decode

fix

?

$$(\alpha|0\rangle + \beta|1\rangle) \otimes |0\rangle \otimes |0\rangle \xrightarrow{U} \alpha|0\rangle \otimes |0\rangle \otimes |0\rangle + \beta|1\rangle \otimes |1\rangle \otimes |1\rangle$$

$$\begin{aligned} \alpha|000\rangle + \beta|111\rangle &\xrightarrow{\text{XII}} \alpha|100\rangle + \beta|011\rangle \rightarrow (\alpha|1\rangle + \beta|0\rangle) |11\rangle \rightarrow (\alpha|0\rangle + \beta|1\rangle) |11\rangle \\ \alpha|000\rangle + \beta|111\rangle &\xrightarrow{\text{IXI}} \alpha|010\rangle + \beta|101\rangle \rightarrow (\alpha|0\rangle + \beta|1\rangle) |10\rangle \rightarrow (\alpha|0\rangle + \beta|1\rangle) |10\rangle \\ \alpha|000\rangle + \beta|111\rangle &\xrightarrow{\text{IIX}} \alpha|001\rangle + \beta|110\rangle \rightarrow (\alpha|0\rangle + \beta|1\rangle) |01\rangle \rightarrow (\alpha|0\rangle + \beta|1\rangle) |01\rangle \end{aligned}$$



# Bit flip Circuit

1	0	0	0
0	1	0	0
0	0	0	1
0	0	1	0

$\alpha|0\rangle + \beta|1\rangle$   
 $|0\rangle$   
 $|0\rangle$

encode

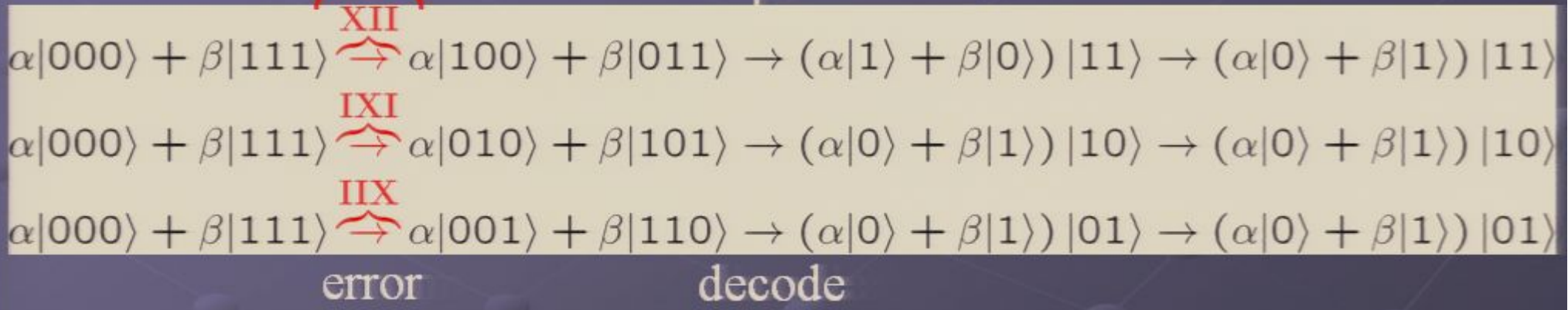
error

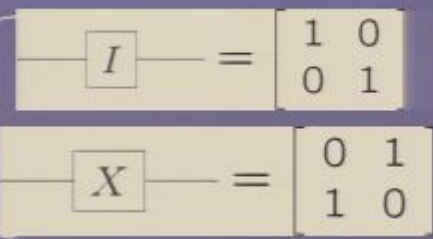
decode

fix

?

$$(\alpha|0\rangle + \beta|1\rangle) \otimes |0\rangle \otimes |0\rangle \xrightarrow{U} \alpha|0\rangle \otimes |0\rangle \otimes |0\rangle + \beta|1\rangle \otimes |1\rangle \otimes |1\rangle$$





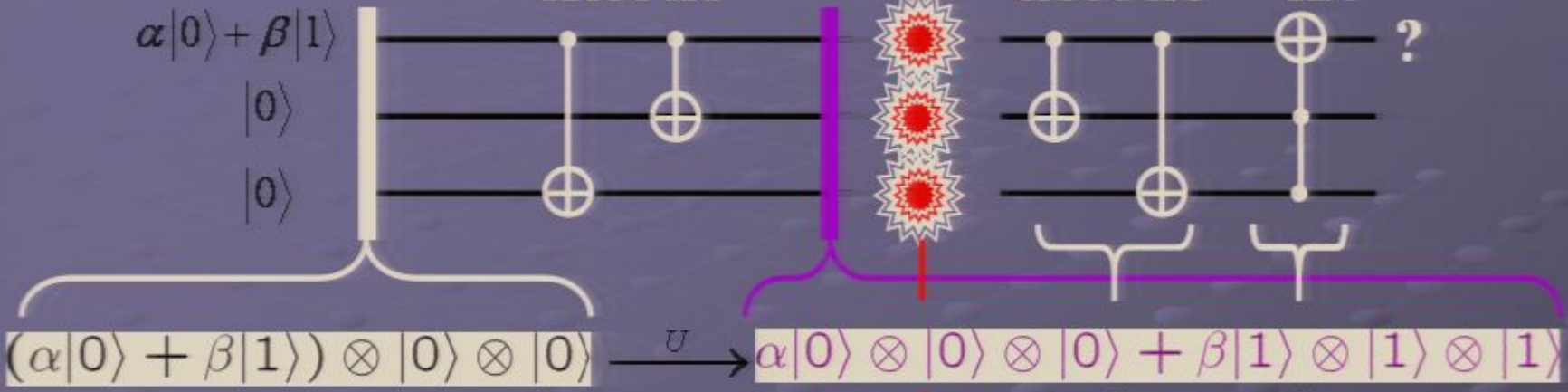
# Bit flip Circuit



1	0	0	0
0	1	0	0
0	0	0	1
0	0	1	0

$\alpha|0\rangle + \beta|1\rangle$   
 $|0\rangle$   
 $|0\rangle$

encode      error      decode      fix



$\alpha|000\rangle + \beta|111\rangle \xrightarrow{XII} \alpha|100\rangle + \beta|011\rangle \rightarrow (\alpha|1\rangle + \beta|0\rangle) |11\rangle \rightarrow (\alpha|0\rangle + \beta|1\rangle) |11\rangle$   
 $\alpha|000\rangle + \beta|111\rangle \xrightarrow{IXI} \alpha|010\rangle + \beta|101\rangle \rightarrow (\alpha|0\rangle + \beta|1\rangle) |10\rangle \rightarrow (\alpha|0\rangle + \beta|1\rangle) |10\rangle$   
 $\alpha|000\rangle + \beta|111\rangle \xrightarrow{IIX} \alpha|001\rangle + \beta|110\rangle \rightarrow (\alpha|0\rangle + \beta|1\rangle) |01\rangle \rightarrow (\alpha|0\rangle + \beta|1\rangle) |01\rangle$

error      decode      fix

1. encoded into subspace:

$|0\rangle \rightarrow |000\rangle, |1\rangle \rightarrow |111\rangle$

(no-cloning evaded!)



$$I = \begin{bmatrix} 1 & 0 \\ 0 & 1 \end{bmatrix}$$

$$X = \begin{bmatrix} 0 & 1 \\ 1 & 0 \end{bmatrix}$$

# Bit flip Circuit

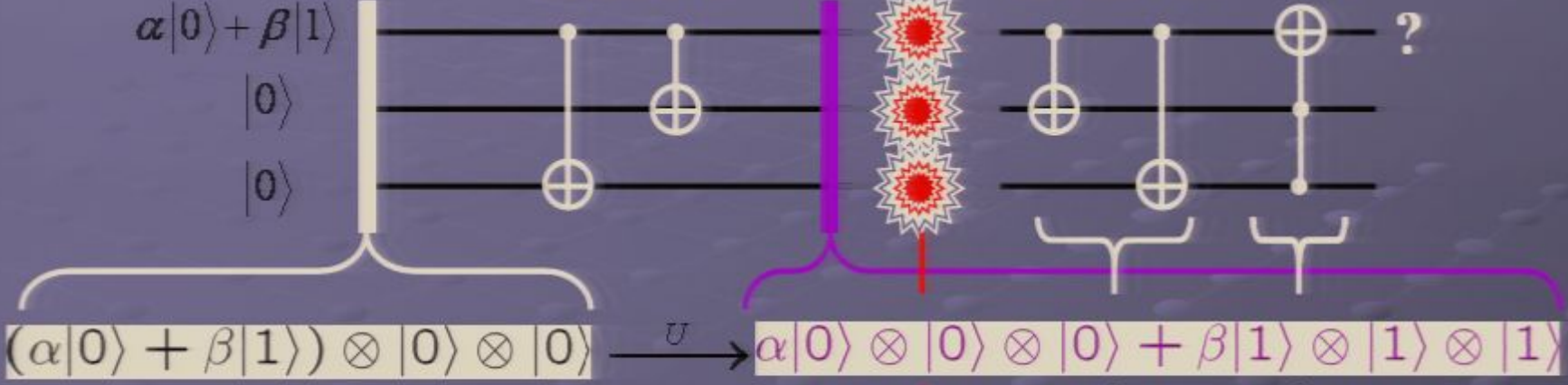
1	0	0	0
0	1	0	0
0	0	0	1
0	0	1	0

$$\alpha|0\rangle + \beta|1\rangle$$

$$|0\rangle$$

$$|0\rangle$$

encode      error      decode      fix



$$(\alpha|0\rangle + \beta|1\rangle) \otimes |0\rangle \otimes |0\rangle \xrightarrow{U} \alpha|0\rangle \otimes |0\rangle \otimes |0\rangle + \beta|1\rangle \otimes |1\rangle \otimes |1\rangle$$

$$\alpha|000\rangle + \beta|111\rangle \xrightarrow{\text{XII}} \alpha|100\rangle + \beta|011\rangle \rightarrow (\alpha|1\rangle + \beta|0\rangle) |11\rangle \rightarrow (\alpha|0\rangle + \beta|1\rangle) |11\rangle$$

$$\alpha|000\rangle + \beta|111\rangle \xrightarrow{\text{IXI}} \alpha|010\rangle + \beta|101\rangle \rightarrow (\alpha|0\rangle + \beta|1\rangle) |10\rangle \rightarrow (\alpha|0\rangle + \beta|1\rangle) |10\rangle$$

$$\alpha|000\rangle + \beta|111\rangle \xrightarrow{\text{IIX}} \alpha|001\rangle + \beta|110\rangle \rightarrow (\alpha|0\rangle + \beta|1\rangle) |01\rangle \rightarrow (\alpha|0\rangle + \beta|1\rangle) |01\rangle$$

error      decode      fix

1. encoded into subspace:

2. errors take to orthogonal subspaces + maintain orthogonality

$$|0\rangle \rightarrow |000\rangle, |1\rangle \rightarrow |111\rangle$$

(no-cloning evaded!) XII|000⟩ = |100⟩, XII|111⟩ = |011⟩



$$I = \begin{bmatrix} 1 & 0 \\ 0 & 1 \end{bmatrix}$$

$$X = \begin{bmatrix} 0 & 1 \\ 1 & 0 \end{bmatrix}$$

# Bit flip Circuit

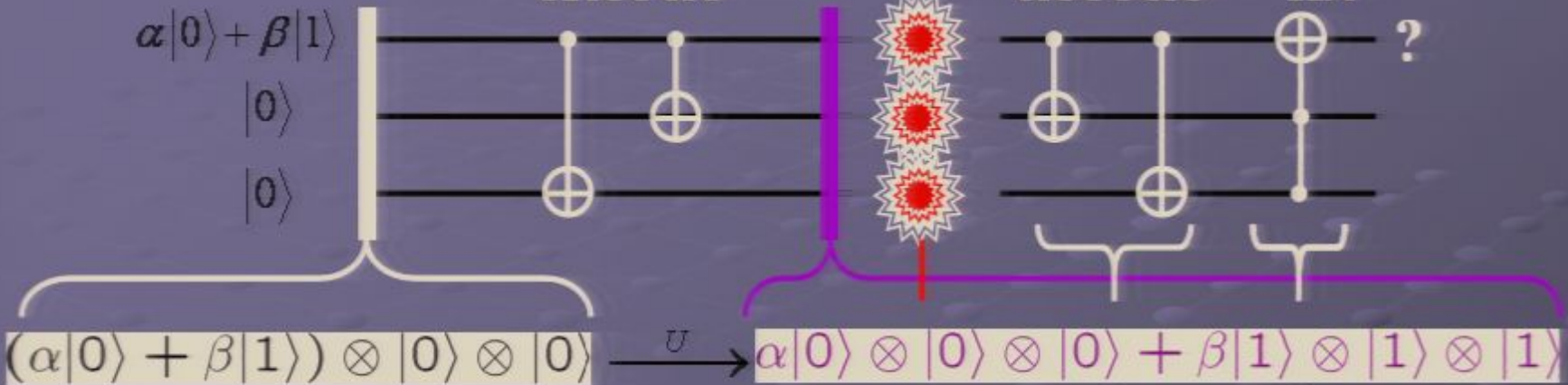
1	0	0	0
0	1	0	0
0	0	0	1
0	0	1	0

$$\alpha|0\rangle + \beta|1\rangle$$

$$|0\rangle$$

$$|0\rangle$$

encode      error      decode      fix



$$(\alpha|0\rangle + \beta|1\rangle) \otimes |0\rangle \otimes |0\rangle \xrightarrow{U} \alpha|0\rangle \otimes |0\rangle \otimes |0\rangle + \beta|1\rangle \otimes |1\rangle \otimes |1\rangle$$

$$\alpha|000\rangle + \beta|111\rangle \xrightarrow{XII} \alpha|100\rangle + \beta|011\rangle \rightarrow (\alpha|1\rangle + \beta|0\rangle) |11\rangle \rightarrow (\alpha|0\rangle + \beta|1\rangle) |11\rangle$$

$$\alpha|000\rangle + \beta|111\rangle \xrightarrow{IXI} \alpha|010\rangle + \beta|101\rangle \rightarrow (\alpha|0\rangle + \beta|1\rangle) |10\rangle \rightarrow (\alpha|0\rangle + \beta|1\rangle) |10\rangle$$

$$\alpha|000\rangle + \beta|111\rangle \xrightarrow{IIX} \alpha|001\rangle + \beta|110\rangle \rightarrow (\alpha|0\rangle + \beta|1\rangle) |01\rangle \rightarrow (\alpha|0\rangle + \beta|1\rangle) |01\rangle$$

error      decode      fix

1. encoded into subspace:
2. errors take to orthogonal subspaces + maintain orthogonality
3. syndrome


$$|0\rangle \rightarrow |000\rangle, |1\rangle \rightarrow |111\rangle$$

$$XII|000\rangle = |100\rangle, XII|111\rangle = |011\rangle$$

(no-cloning evaded!)

# Bit flip an

# Bit flip and Phase flip errors


$$\begin{array}{l} \boxed{I} = \begin{array}{|c|c|} \hline 1 & 0 \\ \hline 0 & 1 \\ \hline \end{array} \\ \boxed{Z} = \begin{array}{|c|c|} \hline 1 & 0 \\ \hline 0 & -1 \\ \hline \end{array} \end{array}$$



# Bit flip and Phase flip errors



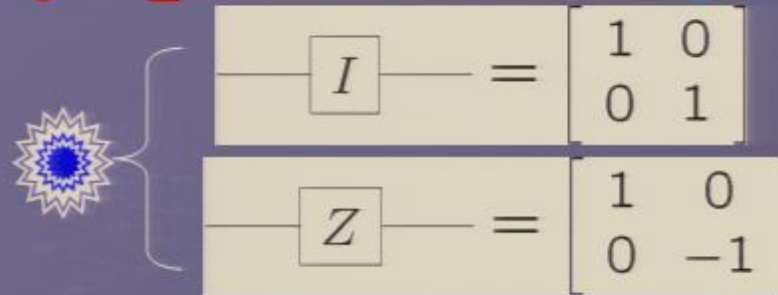
$$\boxed{I} = \begin{pmatrix} 1 & 0 \\ 0 & 1 \end{pmatrix}$$

$$\boxed{Z} = \begin{pmatrix} 1 & 0 \\ 0 & -1 \end{pmatrix}$$

$$Z|0\rangle = |0\rangle$$

$$Z|1\rangle = -|1\rangle$$

# Bit flip and Phase flip errors



$$\begin{pmatrix} 1 & 0 \\ 0 & 1 \end{pmatrix}$$

$$Z|0\rangle = |0\rangle$$

$$\begin{pmatrix} 1 & 0 \\ 0 & -1 \end{pmatrix}$$


$$Z|1\rangle = -|1\rangle$$



basis change :

$$\begin{aligned} |+\rangle &= \frac{1}{\sqrt{2}} (|0\rangle + |1\rangle) \\ |-\rangle &= \frac{1}{\sqrt{2}} (|0\rangle - |1\rangle) \end{aligned}$$

# Bit flip and Phase flip errors



$$\begin{array}{l}
 \boxed{I} = \begin{pmatrix} 1 & 0 \\ 0 & 1 \end{pmatrix} \\
 \boxed{Z} = \begin{pmatrix} 1 & 0 \\ 0 & -1 \end{pmatrix}
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basis change :

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 |+\rangle = \frac{1}{\sqrt{2}}(|0\rangle + |1\rangle) \\
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 \end{array}$$

$$\begin{array}{l}
 Z|+\rangle = |-\rangle \\
 Z|-\rangle = |+\rangle
 \end{array}$$

looks like bit flip error in this new basis!

# Bit flip and Phase flip errors



$$\begin{array}{l}
 \boxed{I} = \begin{pmatrix} 1 & 0 \\ 0 & 1 \end{pmatrix} \\
 \boxed{Z} = \begin{pmatrix} 1 & 0 \\ 0 & -1 \end{pmatrix}
 \end{array}$$

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basis change :


$$\begin{array}{l}
 |+\rangle = \frac{1}{\sqrt{2}}(|0\rangle + |1\rangle) \\
 |-\rangle = \frac{1}{\sqrt{2}}(|0\rangle - |1\rangle)
 \end{array}$$

$$\begin{array}{l}
 Z|+\rangle = |-\rangle \\
 Z|-\rangle = |+\rangle
 \end{array}$$

looks like bit flip error in this new basis!

$$HZH = X, \quad H = |+\rangle\langle 0| + |-\rangle\langle 1|$$

# Bit flip and Phase flip errors



$$\begin{aligned} \boxed{I} &= \begin{pmatrix} 1 & 0 \\ 0 & 1 \end{pmatrix} \\ \boxed{Z} &= \begin{pmatrix} 1 & 0 \\ 0 & -1 \end{pmatrix} \end{aligned}$$

$$Z|0\rangle = |0\rangle$$

$$Z|1\rangle = -|1\rangle$$



basis change :

$$\begin{aligned} |+\rangle &= \frac{1}{\sqrt{2}} (|0\rangle + |1\rangle) \\ |-\rangle &= \frac{1}{\sqrt{2}} (|0\rangle - |1\rangle) \end{aligned}$$

$$\begin{aligned} Z|+\rangle &= |-\rangle \\ Z|-\rangle &= |+\rangle \end{aligned}$$

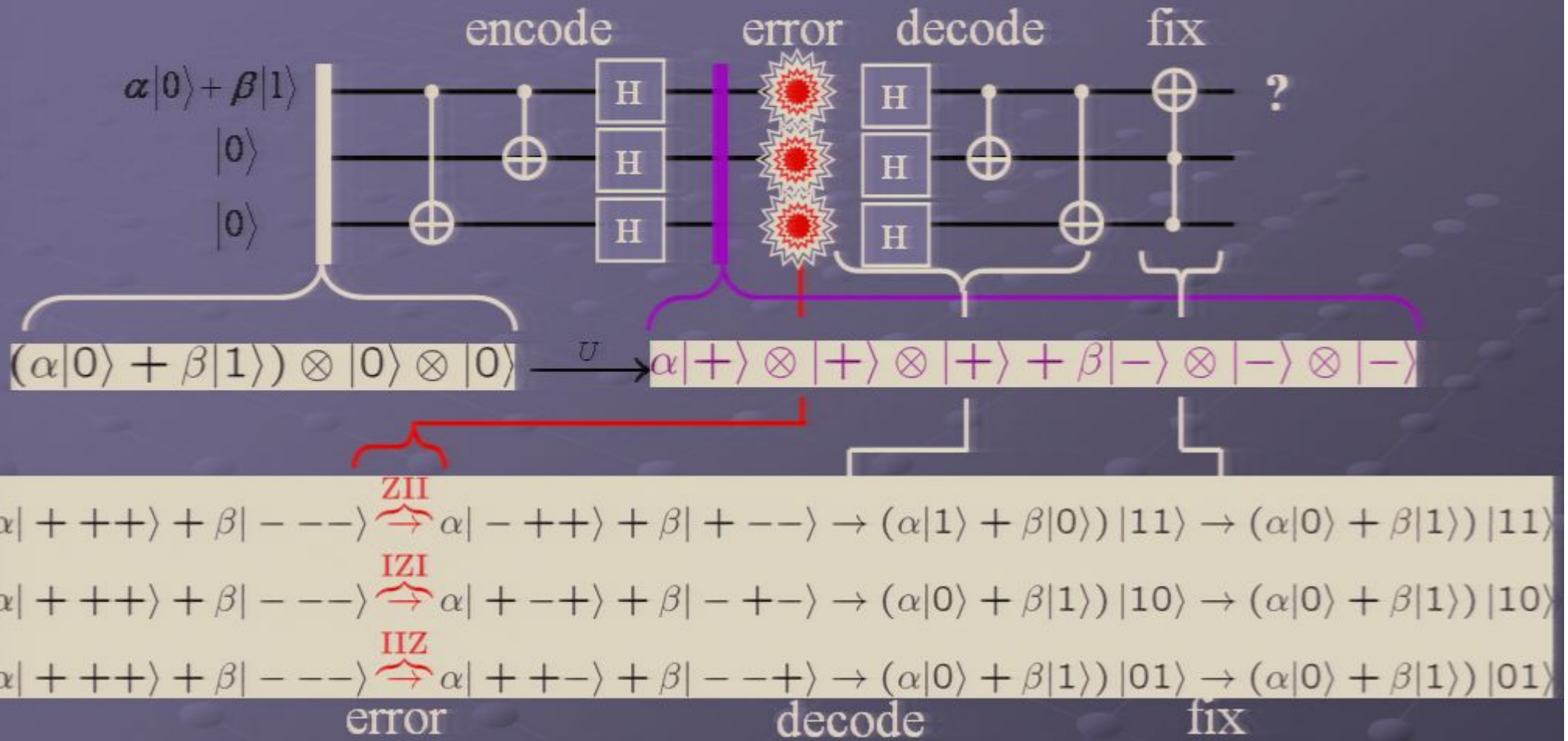
looks like bit flip error in this new basis!

$$\boxed{HZH = X, \quad H = |+\rangle\langle 0| + |-\rangle\langle 1|}$$

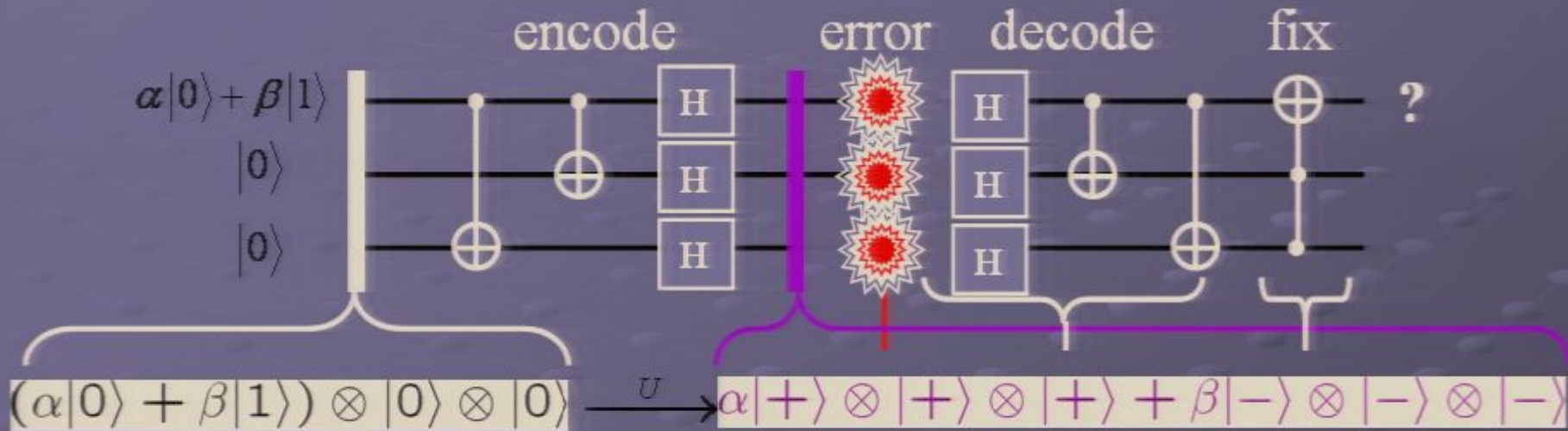


# Phase errors circ

# Phase errors circuit



# Phase errors circuit



$$\begin{aligned}
 &\alpha|+++ \rangle + \beta|--- \rangle \xrightarrow{ZII} \alpha| - ++ \rangle + \beta| + -- \rangle \rightarrow (\alpha|1\rangle + \beta|0\rangle) |11\rangle \rightarrow (\alpha|0\rangle + \beta|1\rangle) |11\rangle \\
 &\alpha|+++ \rangle + \beta|--- \rangle \xrightarrow{IZI} \alpha| + - + \rangle + \beta| - + - \rangle \rightarrow (\alpha|0\rangle + \beta|1\rangle) |10\rangle \rightarrow (\alpha|0\rangle + \beta|1\rangle) |10\rangle \\
 &\alpha|+++ \rangle + \beta|--- \rangle \xrightarrow{IIZ} \alpha| + + - \rangle + \beta| - - + \rangle \rightarrow (\alpha|0\rangle + \beta|1\rangle) |01\rangle \rightarrow (\alpha|0\rangle + \beta|1\rangle) |01\rangle
 \end{aligned}$$

error
decode
fix

1. encoded into subspace:

2. errors take to orthogonal

$|0\rangle \rightarrow |+++ \rangle, |1\rangle \rightarrow |--- \rangle$  subspaces + maintain orthogonality 3. syndrome

(no-cloning evaded!)  $ZII|+++ \rangle = |-++ \rangle, ZII|--- \rangle = |+-- \rangle$



# Shor's

# Shor's code summary

## 3 qubit bit flip code

$$|0\rangle \rightarrow |000\rangle$$

$$|1\rangle \rightarrow |111\rangle$$

## 3 qubit phase flip code

$$|0\rangle \rightarrow |+++ \rangle$$

$$|1\rangle \rightarrow |-- -- \rangle$$

# Shor's code summary

3 qubit bit flip code

$$\begin{aligned} |0\rangle &\rightarrow |000\rangle \\ |1\rangle &\rightarrow |111\rangle \end{aligned}$$

3 qubit phase flip code

$$\begin{aligned} |0\rangle &\rightarrow |+++ \rangle \\ |1\rangle &\rightarrow |-- -- \rangle \end{aligned}$$

phase errors  $ZII$ ,  $IZI$ ,  $IIZ$  act as  $Z$  on bit flip code qubits:

$$\begin{aligned} |0\rangle_B &= |000\rangle & ZII|0\rangle_B &= |0\rangle_B \\ |1\rangle_B &= |111\rangle & ZII|1\rangle_B &= -|1\rangle_B \end{aligned}$$

# Shor's code summary

3 qubit bit flip code

$$\begin{aligned} |0\rangle &\rightarrow |000\rangle \\ |1\rangle &\rightarrow |111\rangle \end{aligned}$$

3 qubit phase flip code

$$\begin{aligned} |0\rangle &\rightarrow |+++ \rangle \\ |1\rangle &\rightarrow |-- -- \rangle \end{aligned}$$

phase errors  $ZII, IZI, IIZ$  act as  $Z$  on bit flip code qubits:

$$\begin{aligned} |0\rangle_B &= |000\rangle & ZII|0\rangle_B &= |0\rangle_B \\ |1\rangle_B &= |111\rangle & ZII|1\rangle_B &= -|1\rangle_B \end{aligned}$$

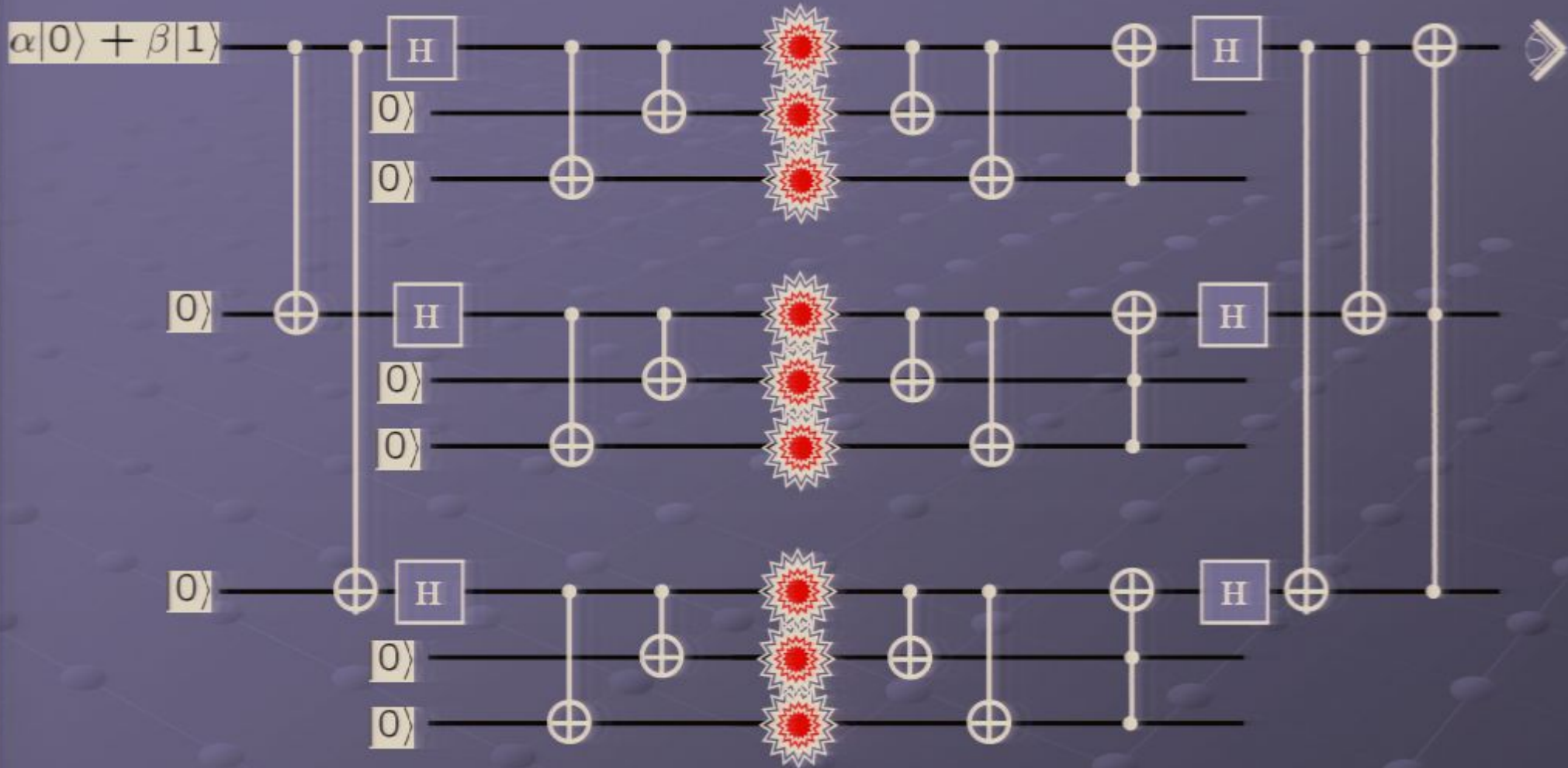
define:

$$\begin{aligned} |p\rangle &= \frac{1}{\sqrt{2}} (|0\rangle_B + |1\rangle_B) = \frac{1}{\sqrt{2}} (|000\rangle + |111\rangle) \\ |m\rangle &= \frac{1}{\sqrt{2}} (|0\rangle_B - |1\rangle_B) = \frac{1}{\sqrt{2}} (|000\rangle - |111\rangle) \end{aligned}$$

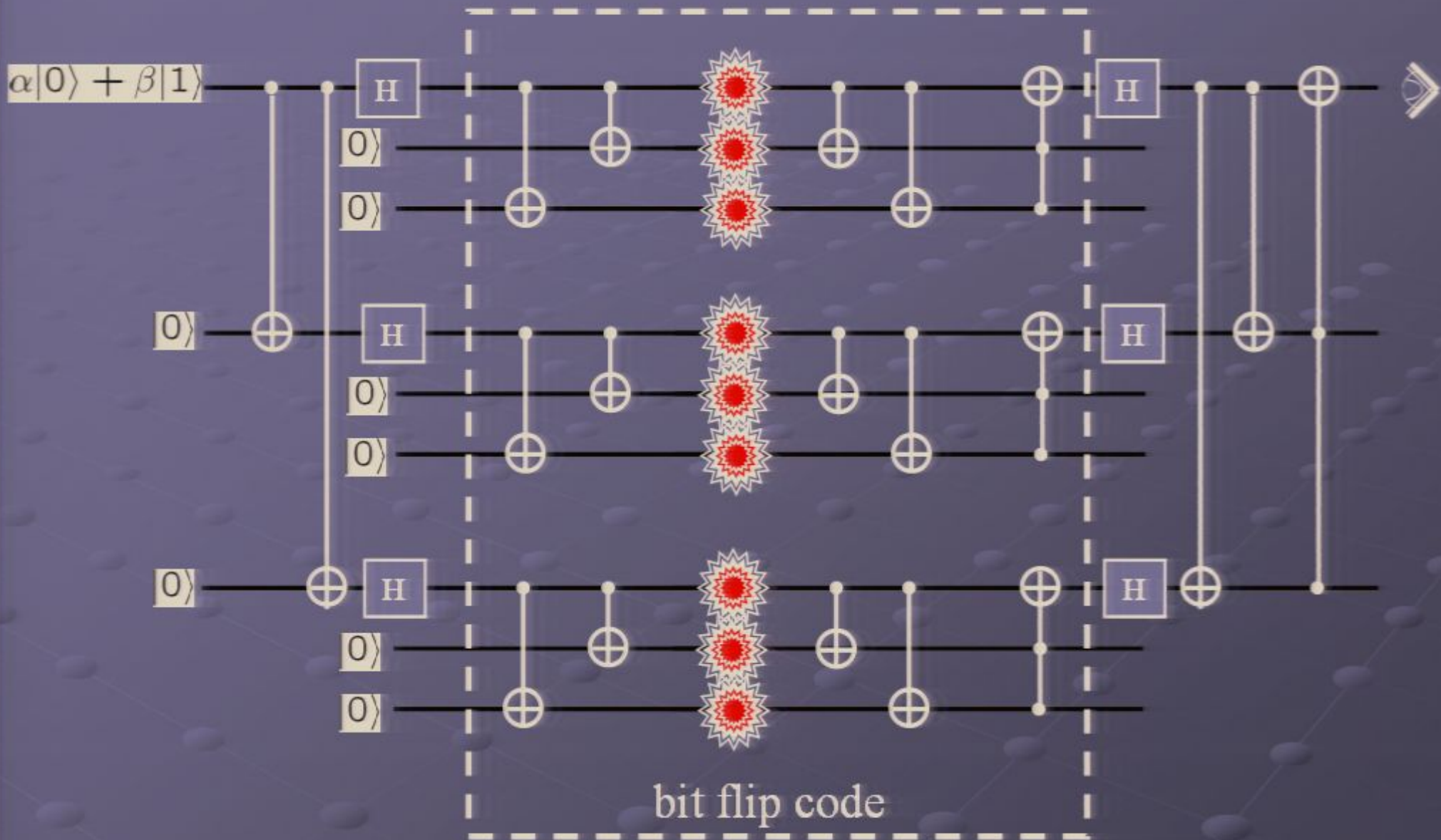
Shor Code: (Peter Shor, 1995)

$$\begin{aligned} |0\rangle &\rightarrow |ppp\rangle = \frac{1}{2\sqrt{2}} (|000\rangle + |111\rangle) (|000\rangle + |111\rangle) (|000\rangle + |111\rangle) \\ |1\rangle &\rightarrow |mmm\rangle = \frac{1}{2\sqrt{2}} (|000\rangle - |111\rangle) (|000\rangle - |111\rangle) (|000\rangle - |111\rangle) \end{aligned}$$

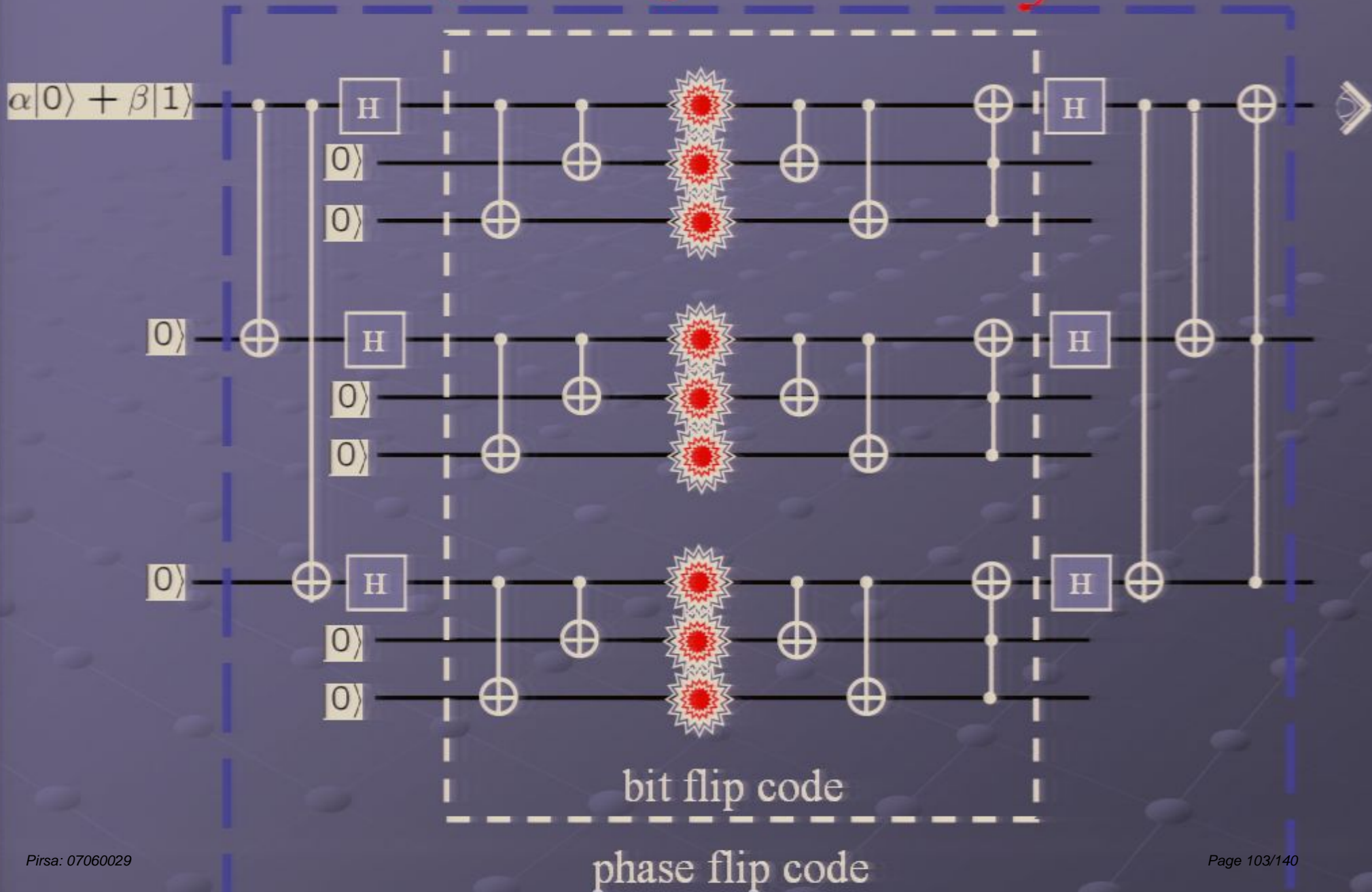
# Shor's Code circuitry



# Shor's Code circuitry



# Shor's Code circuitry



# *Fault Tolerant Quantum Computation*



# Fault Tolerant Quantum computation

# Fault Tolerant Quantum computation

- Fault Tolerance computation requires that if the probability of introducing error in the circuit is  $p$ , the probability that the circuit brings two or more errors grows like  $O(p^2)$ . This means that a fault tolerant procedure comes to end successfully with  $1 - cp^2$  probability where  $c$  depends only on the circuit.

# Fault Tolerant Quantum computation

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- Concatenation methods brings a square benefit in reducing error probability decreasing the factor from  $cp^2$  to  $c(cp^2)^2$ .

# Fault Tolerant Quantum computation

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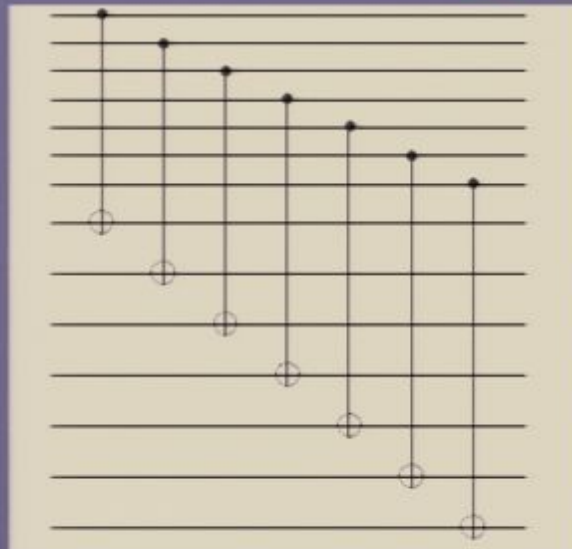
- Arbitrary accuracy could be reached with a dimensional growth that scales as

$$d^a = O\left(\text{poly}\left(\log\left(\frac{1}{c\varepsilon}\right)\right)\right)$$

Threshold

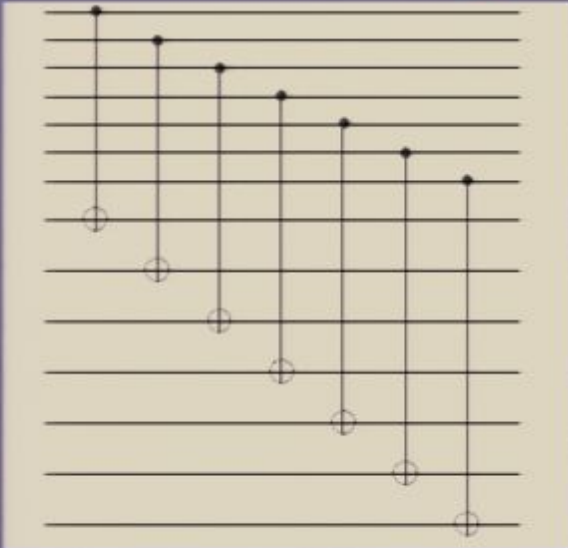
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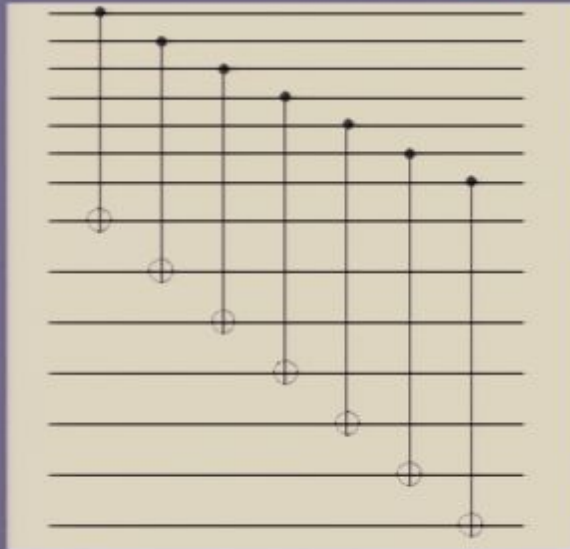
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Fault tolerant rules requires not to introduce more than one error for every encoded block  
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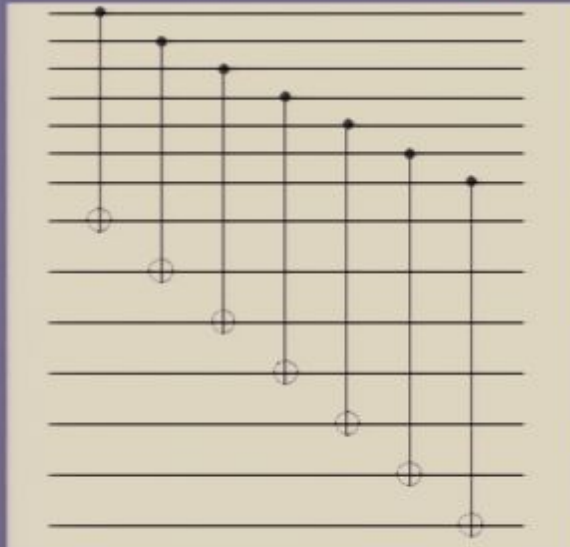
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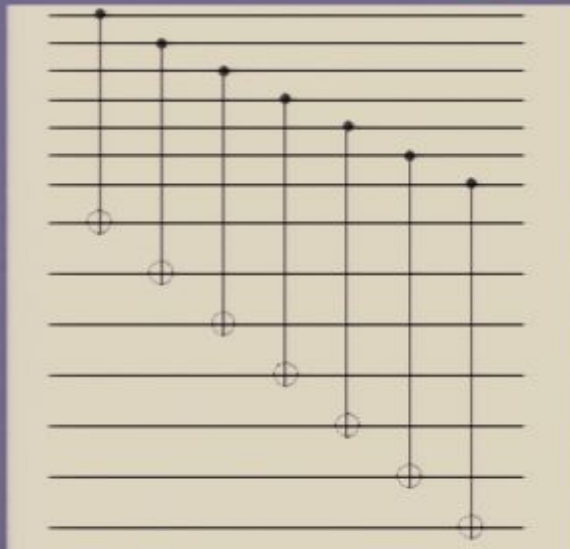
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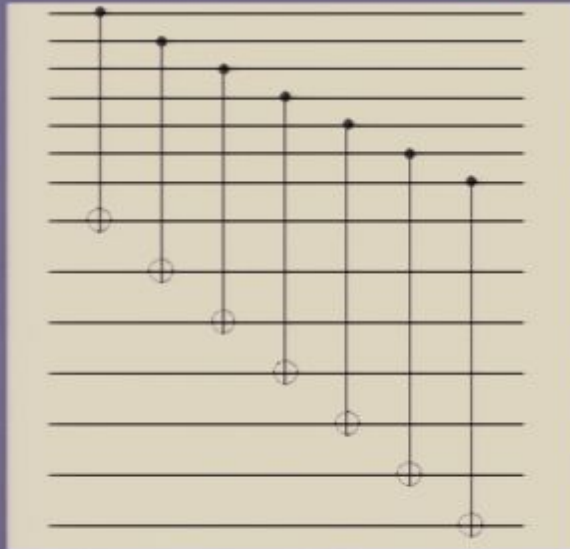
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*Gottesman-Knill theorem has demonstrated the efficient simulation on a classical computer of quantum computation with fault tolerant quantum gates (Clifford group gates: Hadamard, Phase, CNOT).*

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Subsystem Space

$$H = (D \otimes C) \oplus \mathcal{E}$$



Code Space

$$H = \bigoplus_{\substack{\delta_1^X, \dots, \delta_{n-1}^X, \delta_1^Z, \dots, \delta_{n-1}^Z = \pm 1}} H^T_{\delta^X, \delta^Z} \otimes H^L_{\delta^X, \delta^Z}$$



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encoding subsystem

$$(|\phi\rangle\langle\phi| \otimes \rho_D) \oplus 0$$

encoded quantum information



Error Recovery

$$(|\phi\rangle\langle\phi| \otimes \tilde{\rho}_D) \oplus 0$$

Correction conditions

$$\langle \phi_i | E_i^\dagger E_j | \phi_j \rangle = C_{ij} \delta_{ij}$$



$$\langle \phi_i | \otimes \langle \phi_k | E_i^\dagger E_j | \phi_j \rangle \otimes | \phi_l \rangle = C_{ij/d} \delta_{ij}$$

$V_x^n :$

$$|0\rangle_L = |00\dots 0\rangle$$

$$|1\rangle_L = |11\dots 1\rangle$$

$$S_i : ZZI, ZIZ, IZZ$$

## *An example:* *$[n^2, 1, n]$ Shor Code*

- For a  $n$  redundancy code the stabilizers are all pairs of  $Z$  acting on every pair of qubits among the  $n$  qubits in the code block. Similarly for the redundancy code in the Hadamard rotated basis, the stabilizers are all possible pairs of  $X$  acting on  $n$  qubits.

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# $[n^2, 1, n]$ Shor Code (2)

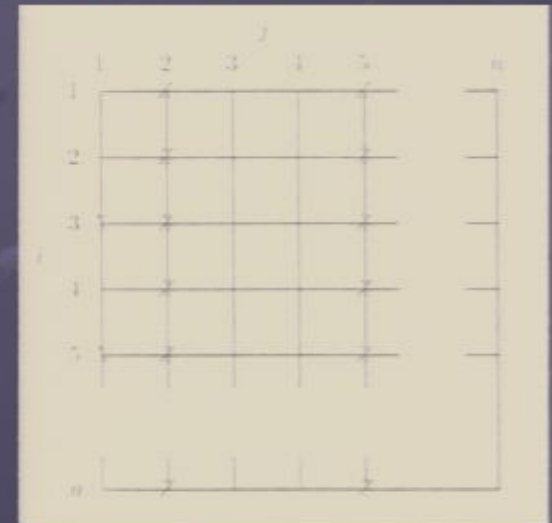
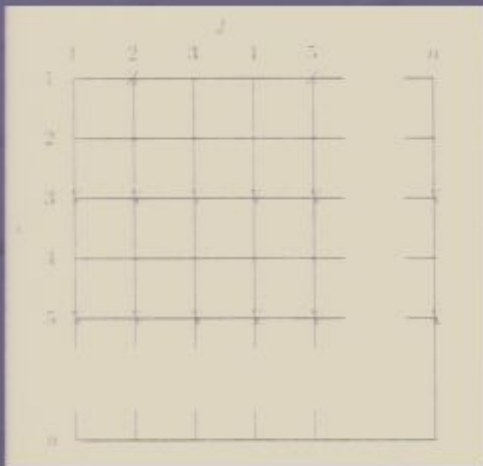
Placing Qubits in different sub-blocks to lie on different rows, the stabilizer includes X operators acting on all qubits in every pair of rows. Furthermore within each sub-block (each row) the stabilizer includes Z operators acting on all pairs of qubits in the corresponding row. Same comments on X basis

More formally Shor's code is generated by

$(n-1)+n(n-1)=n^2-1$  operators

$$S_{C_2 \circ C_z} = \langle Z_{col-j, col-(j+1)}; X_{i,j} X_{i+1,j}; X_{i,n} X_{i+1,n} : i, j \in \mathcal{C}_{n-1} \rangle$$

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Pirsa: 07060029

Z basis

$$|k\rangle \propto |+\rangle + (-1)^k |-\rangle$$

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X basis

35

# Bacon-Shor Codes

- **This code is a generalization of Shor's original quantum error correcting subspace code.**
- It eliminates the asymmetry inside Shor Code in the treatment of  $Z$  errors and  $X$  errors.
- This code is able to correct  $n/2$   $X$  and  $Z$  errors and it's generated



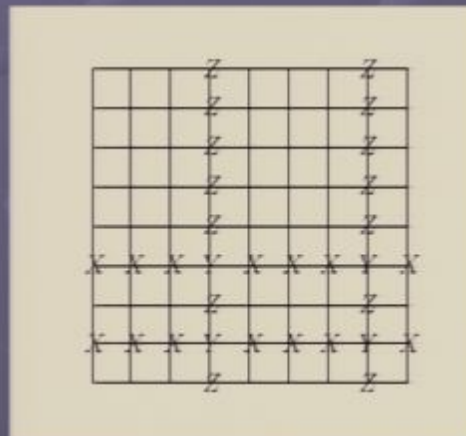
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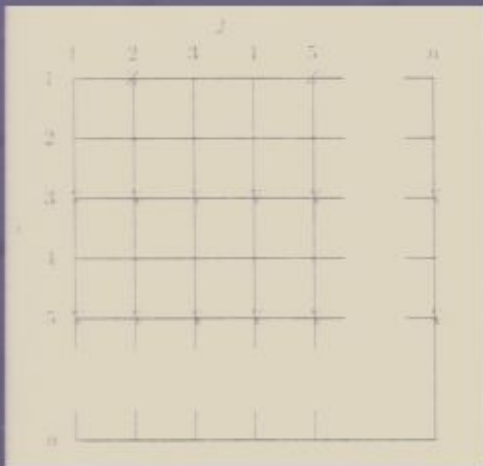
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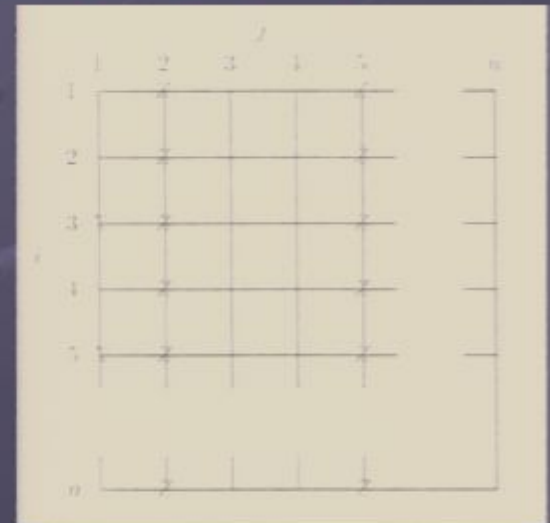
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Shor's code construction treats X and Z errors asymmetrically. Within each of the subblocks up to  $n/2$  errors can be corrected because of the underlying Repetition code. The code can also correct up to  $n/2$  Z errors in different subblocks (viceversa in the X basis)



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35

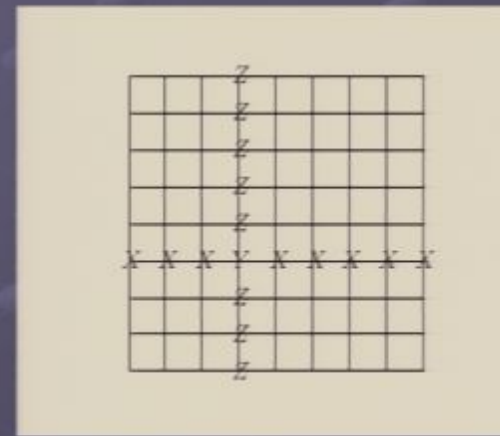
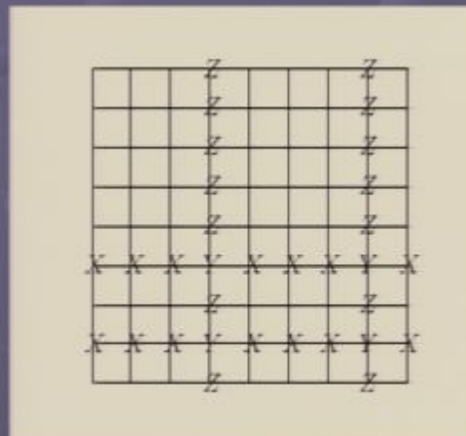
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Z in every couple of columns  
X in every couple of rows



Logical qubits

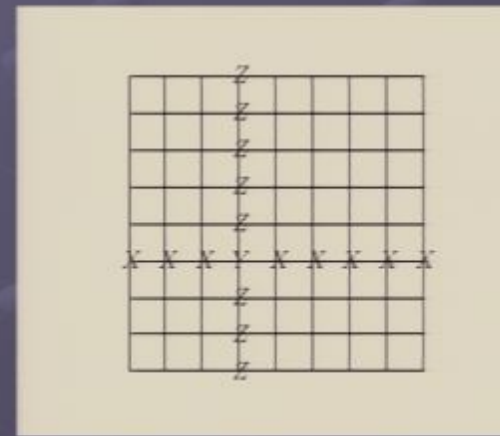
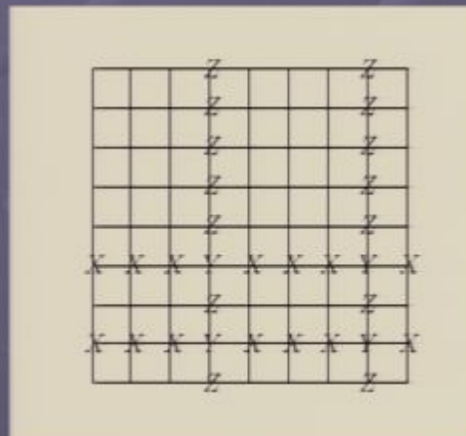
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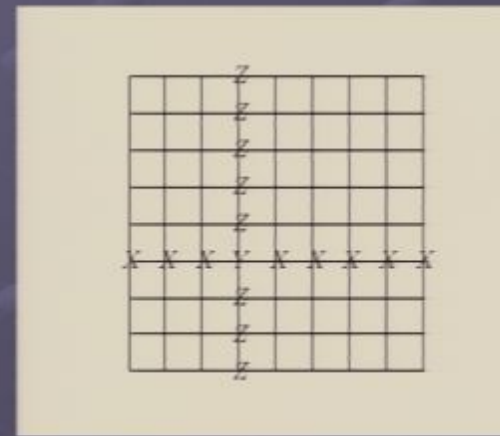
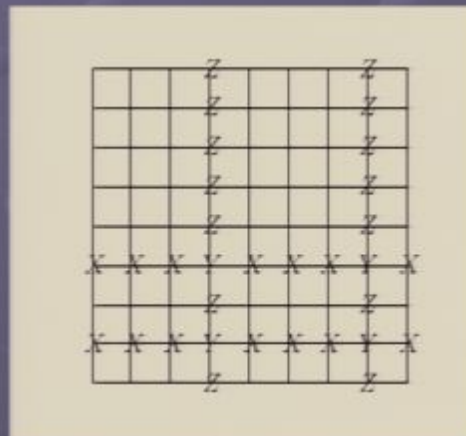
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Subsystem Construction



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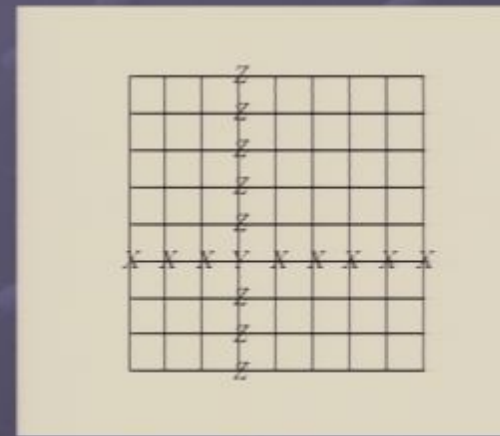
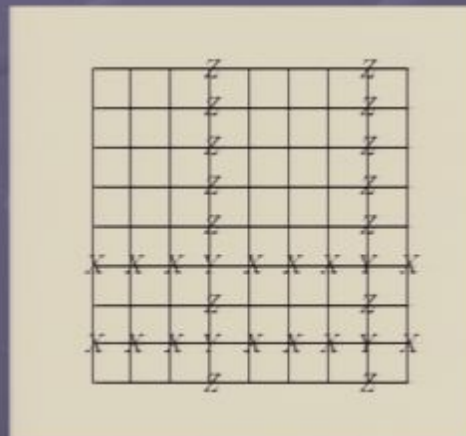
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Subsystem Construction of this code allows simpler correcting routines and interesting fault tolerant properties!!!

# Conclusions and future works

- Generalized construction of the Bacon Shor codes from two classical linear codes (Bacon- Casaccino, 2006)--> *visit me during the poster session!*
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- Generalization of the construction and remarkable properties about Singleton and Quantum Hamming Bound (Klappenecker, Sarvepalli, 2007)
- **Work in progress...**

*Thank you for your attention!!!*



## *A Look to the future ...*

*Only time will tell if and when the problems of building a quantum computer can be overcome. As information becomes the world's most valuable commodity, the economic, political and military fate of nations will depend on the strength of ciphers. Consequently, the development of a fully operational quantum computer would imperil our personal privacy, destroy electronic commerce and demolish the concept of national security. A quantum computer would jeopardize the stability of the world. Whichever country gets there first will have the ability to monitor the communications of its citizens, read the minds of its commercial rivals and eavesdrop on the plans of its enemies. Although it is still in its infancy, quantum computing presents a potential threat to the individual, to international business and to global security. -*

*Simon Singh*

[www.Simonsingh.net](http://www.Simonsingh.net)

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